

FIG. 1

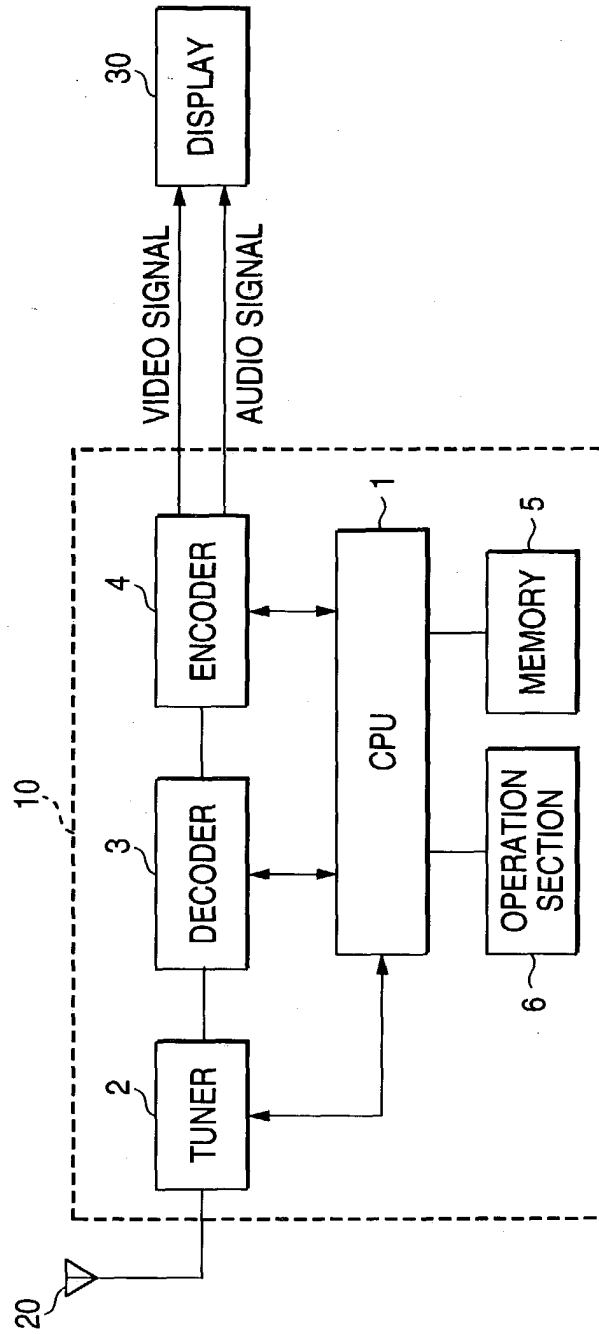


FIG. 2

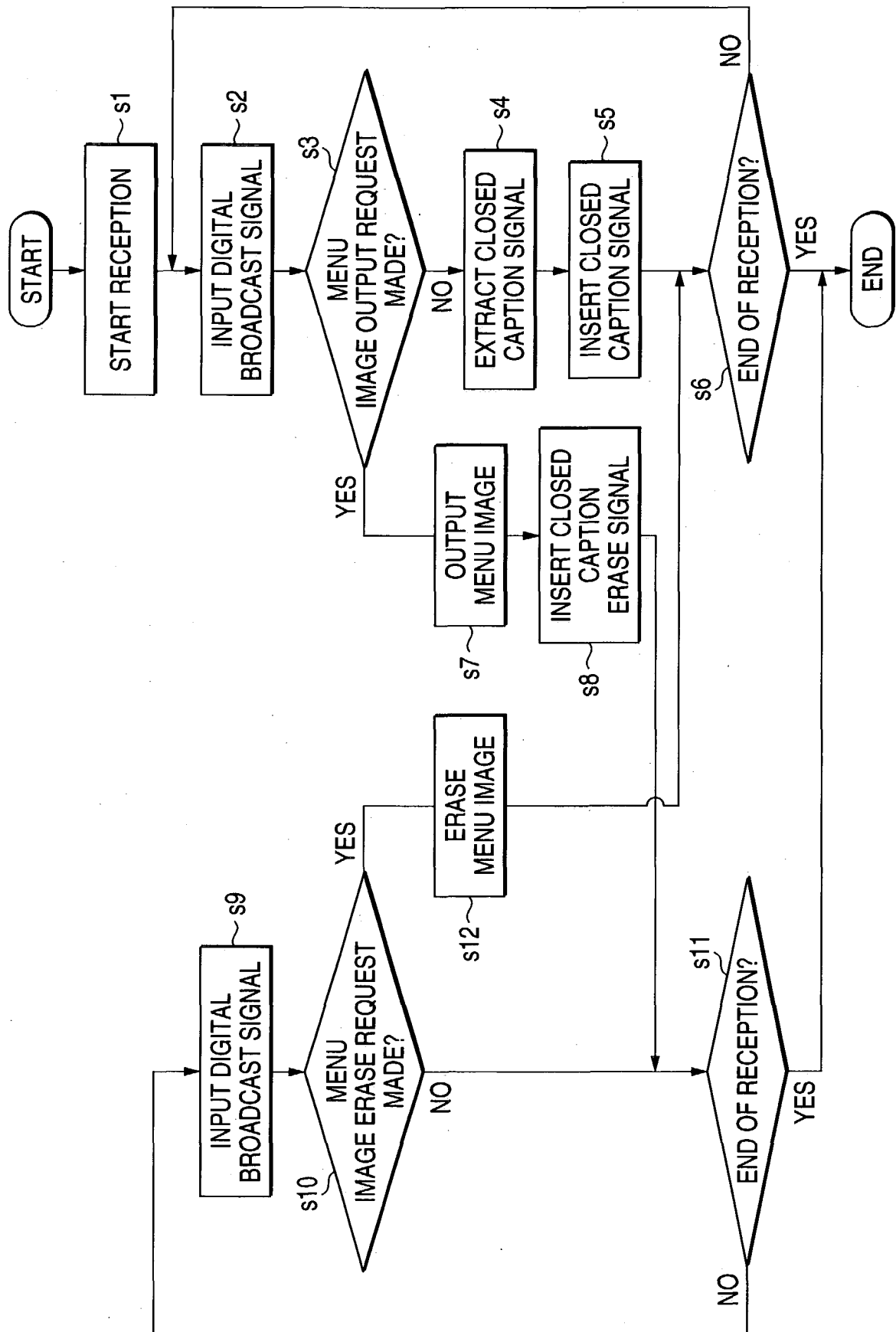


FIG. 3

	CODE 1	CODE 2	DESCRIPTION
1	0 x 14	0 x 2C	ERASING OF DISPLAY MEMORY OF DATA CHANNEL 1
2	0 x 14	0 x 2C	
3	0 x 1C	0 x 2C	ERASING OF DISPLAY MEMORY OF DATA CHANNEL 2
4	0 x 1C	0 x 2C	
5	0 x 14	0 x 2E	ERASING OF NON-DISPLAY MEMORY OF DATA CHANNEL 1
6	0 x 14	0 x 2E	
7	0 x 1C	0 x 2E	ERASING OF NON-DISPLAY MEMORY OF DATA CHANNEL 2
8	0 x 1C	0 x 2E	

FIG. 4

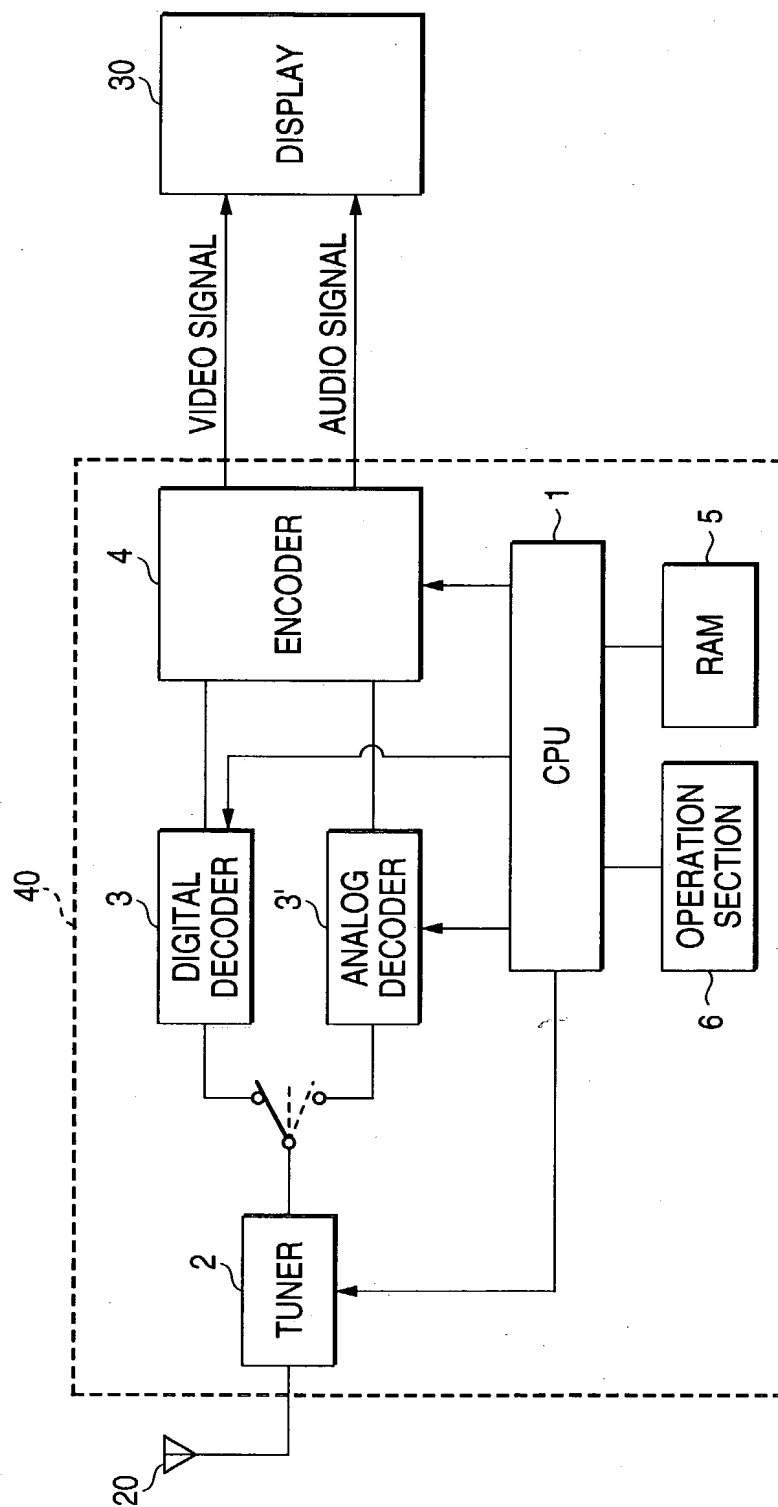


FIG. 5

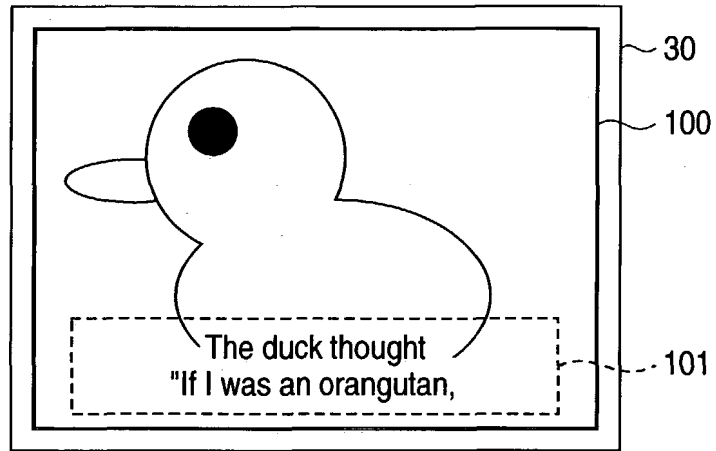


FIG. 6

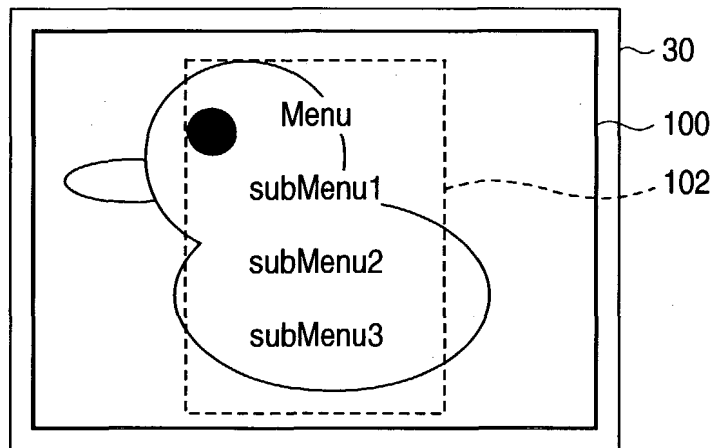


FIG. 7

